Trench Power MOSFET

12 V, 3.3 A, Single P–Channel, ESD Protected SC–88

Features

- Leading Trench Technology for Low R_{DS(ON)} Extending Battery Life
- SC-88 Small Outline (2x2 mm, SC70-6 Equivalent)
- Gate Diodes for ESD Protection
- NV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- High Side Load Switch
- Cell Phones, Computing, Digital Cameras, MP3s and PDAs

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated) Parameter Symbol V

Param	Symbol	Value	Units		
Drain-to-Source Voltage	V _{DSS}	-12	V		
Gate-to-Source Voltage	V _{GS}	±12	V		
Continuous Drain	Steady	T _A = 25 °C	I _D	-2.7	А
Current (Note 1)	State	T _A = 85 °C		-2.0	
	t ≤ 5 s	T _A = 25 °C		-3.3	
Power Dissipation (Note 1) Steady State		T _A = 25 °C	PD	0.625	W
Pulsed Drain Current	I _{DM}	-8.0	А		
Operating Junction and S	T _J , T _{STG}	–55 to 150	°C		
Source Current (Body Diode)			۱ _S	-0.8	А
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			ΤL	260	°C

THERMAL RESISTANCE RATINGS (Note 1)

Parameter	Symbol	Max	Units
Junction-to-Ambient - Steady State	R_{\thetaJA}	200	°C/W
Junction-to-Ambient - t \leq 5 s	R_{\thetaJA}	141	
Junction-to-Lead - Steady State	$R_{ extsf{ heta}JL}$	102	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface mounted on FR4 board using 1 in sq pad size

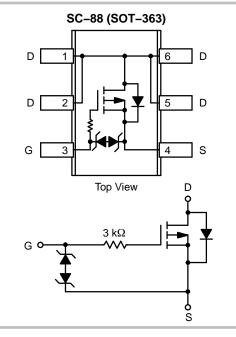
(Cu area = 1.127 in sq [1 oz] including traces).



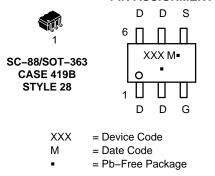
ON Semiconductor®

www.onsemi.com

V _{(BR)DSS}	R _{DS(on)} Тур	I _D Max
	45 mΩ @ –4.5 V	
–12 V	67 mΩ @ –2.5 V	–3.3 A
	133 mΩ @ –1.8 V	



MARKING DIAGRAM & PIN ASSIGNMENT



(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

ELECTRICAL CHARACTERISTICS (T_J=25°C unless otherwise stated)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS					•		
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_D = -250 \mu A$		-12			V
Drain–to–Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				10		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{CS} = -9.6 V_{c}$	$T_J = 25^{\circ}C$			-1.0	
		$V_{GS} = -9.6 V,$ $V_{DS} = 0 V$	T _J = 125°C		-2.5		-
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 V, V_{G}$	_S = ±4.5 V			±1.5	μA
		$V_{DS} = 0 V, V_{C}$	_S = ±12 V			±10	mA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D}$	= 100 μA	-0.40			V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				3.4		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -3.3 \text{ A}$			45	60	mΩ
		V _{GS} = -2.5 V,	$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -2.9 \text{ A}$		67	90	
	V _{GS} = -1.8 V,		_D = -1.0 A		133	160	
Forward Transconductance	9FS	$V_{GS} = -10$ V, $I_D = -3.3$ A			15		S
CHARGES AND CAPACITANCES							
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1.0 MHz, V _{DS} = -12 V			850		pF
Output Capacitance	C _{OSS}				170		1
Reverse Transfer Capacitance	C _{RSS}	- 53			110		
Total Gate Charge	Q _{G(TOT)}				8.6		nC
Gate-to-Source Charge	Q _{GS}	$V_{GS} = -4.5 \text{ V}, \text{ V}$ $I_{D} = -3.5 \text{ V}$	_{DS} = -5.0 V, 3 A		1.3		
Gate-to-Drain Charge	Q _{GD}				2.2		
Gate Resistance	R _G				3000		Ω
SWITCHING CHARACTERISTICS (No	te 3)						
Turn–On Delay Time	t _{d(ON)}	V_{GS} = -4.5 V, V_{DD} = -6.0 V, I _D = -1.0 A, R _G = 6.0 Ω			0.86		μs
Rise Time	t _r				1.5		
Turn-Off Delay Time	t _{d(OFF)}				3.5		
Fall Time	t _f				3.9		
DRAIN-SOURCE DIODE CHARACTE	RISTICS (Note 2	2)			- -		•
Forward Diode Voltage	V _{SD}	$V_{GS} = 0 V$, $T_{J} = 25^{\circ}C$			-0.85	-1.2	V

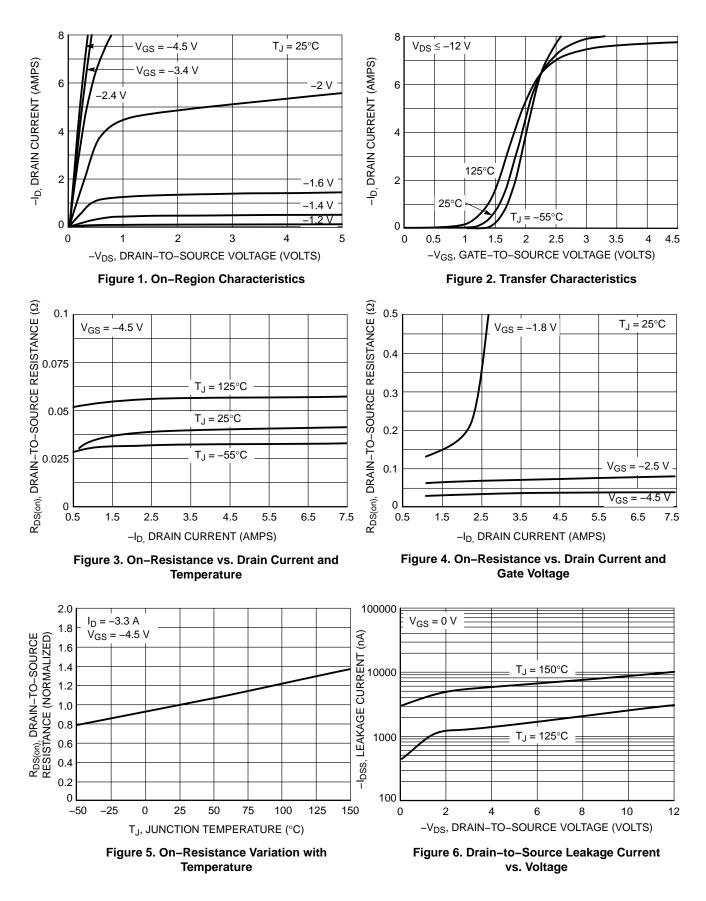
Forward Diode Voltage V_{SD} $V_{GS} = 0 V$,
 $I_S = -3.3 A$ $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$ $T_J = 125^{\circ}C$

 $I_{S} = -3.3 \text{ A} \qquad T_{J} = 125^{\circ}\text{C} \qquad -0.7$ Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

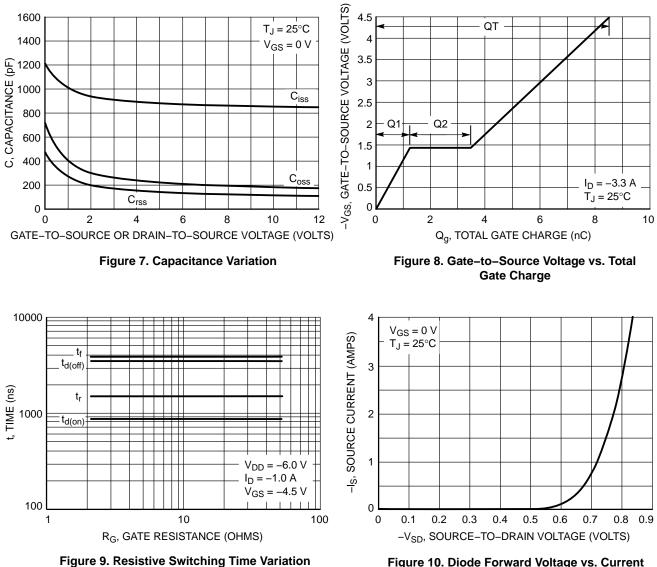
2. Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%.

3. Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



vs. Gate Resistance

Figure 10. Diode Forward Voltage vs. Current

ORDERING INFORMATION

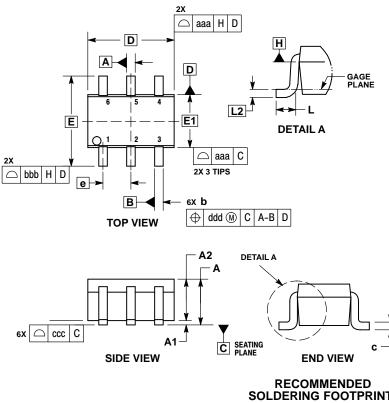
Device	Marking	Package	Shipping [†]	
NTJS3151PT1G	TJ			
NTJS3151PT2G	TJ	SC–88 (Pb–Free)	3000 / Tape & Reel	
NVJS3151PT1G*	VTJ			

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363 CASE 419B-02 ISSUE Y



NOTES

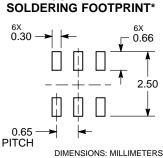
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 1. 2. 3. CONTROLLING DIMENSION: MILLIMETERS. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRU-SIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF 4
- THE PLASTIC BODY AND DATUM H. DATUMS A AND B ARE DETERMINED AT DATUM H. DIMENSIONS & AND B ARE DETERMINED AT DATUM H. DIMENSIONS & AND CAPPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP. 6.
- DIMENSION & DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN 7 EXCESS OF DIMENSION 6 AT MAXIMUM MATERIAL CONDI-TION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α			1.10			0.043	
A1	0.00		0.10	0.000		0.004	
A2	0.70	0.90	1.00	0.027	0.035	0.039	
b	0.15	0.20	0.25	0.006	0.008	0.010	
С	0.08	0.15	0.22	0.003	0.006	0.009	
D	1.80	2.00	2.20	0.070	0.078	0.086	
Е	2.00	2.10	2.20	0.078	0.082	0.086	
E1	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65 BSC			0.026 BSC			
L	0.26	0.36	0.46	0.010	0.014	0.018	
L2	0.15 BSC			0.006 BSC			
aaa	0.15			0.006			
bbb	0.30			0.012			
ccc	0.10			0.004			
ddd	0.10				0.004		

STYLE 28: PIN 1. DRAIN 2. DRAIN 3. GATE



DRAIN 6.



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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